

FIG. 2

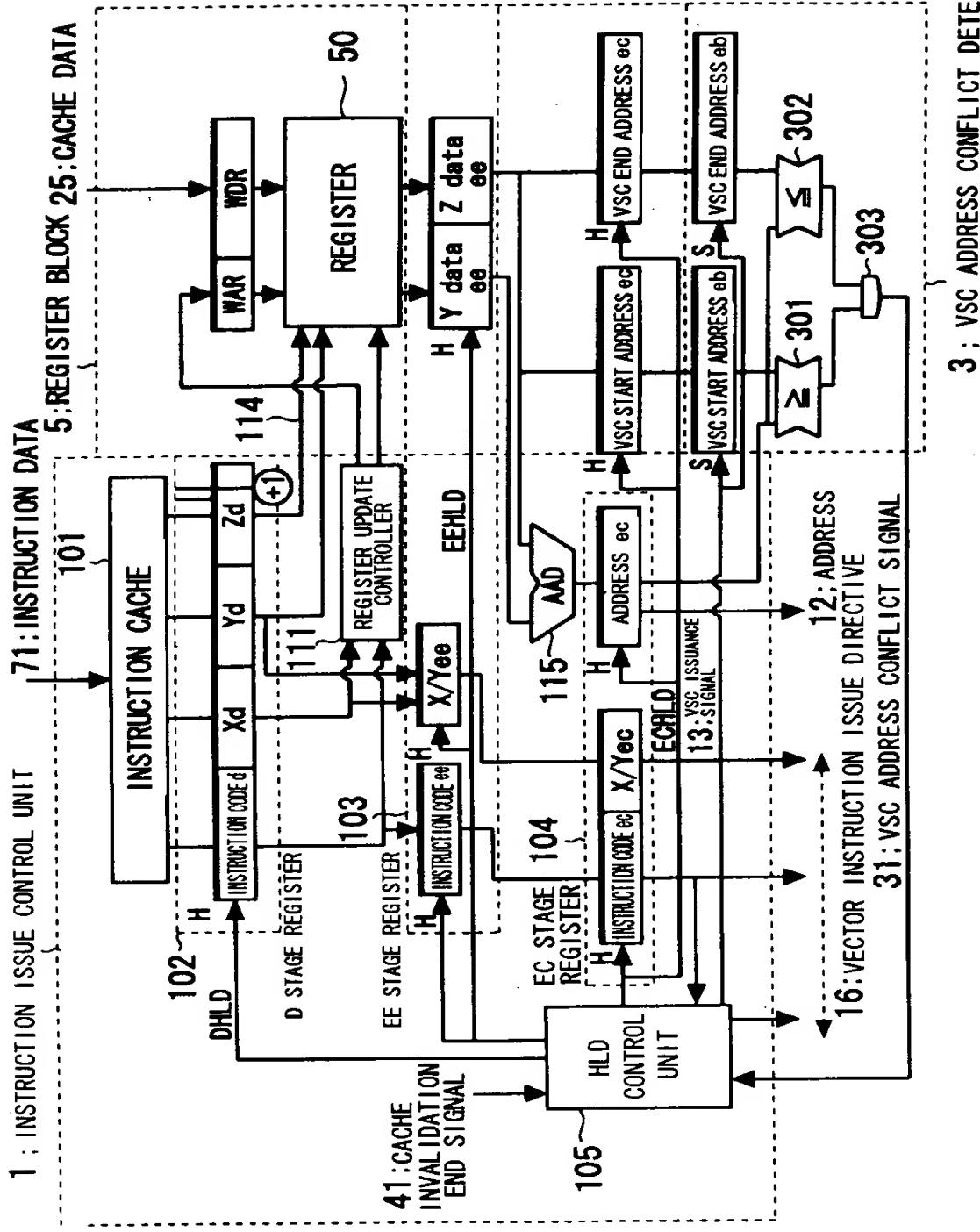


FIG . 3

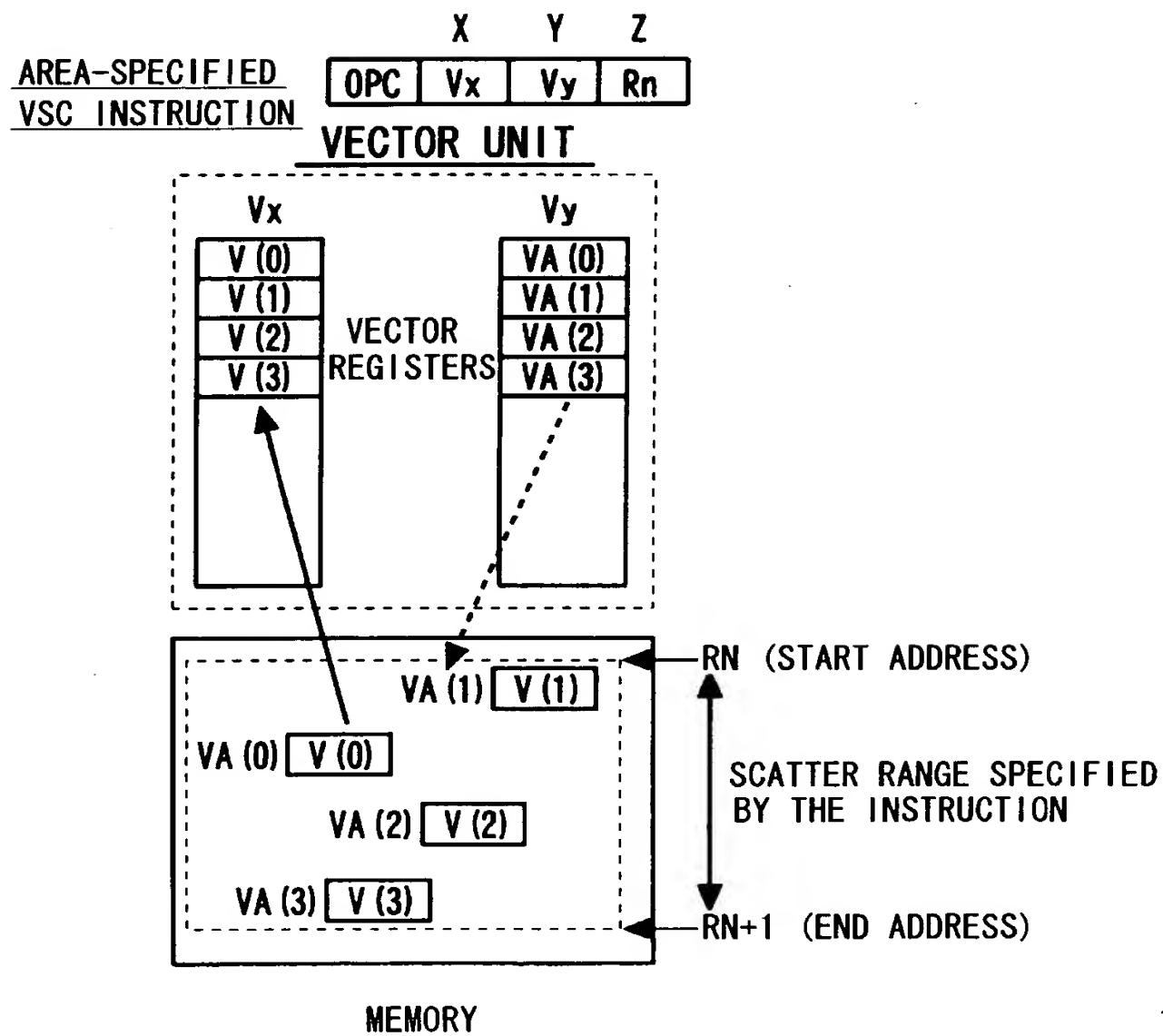


FIG : 4

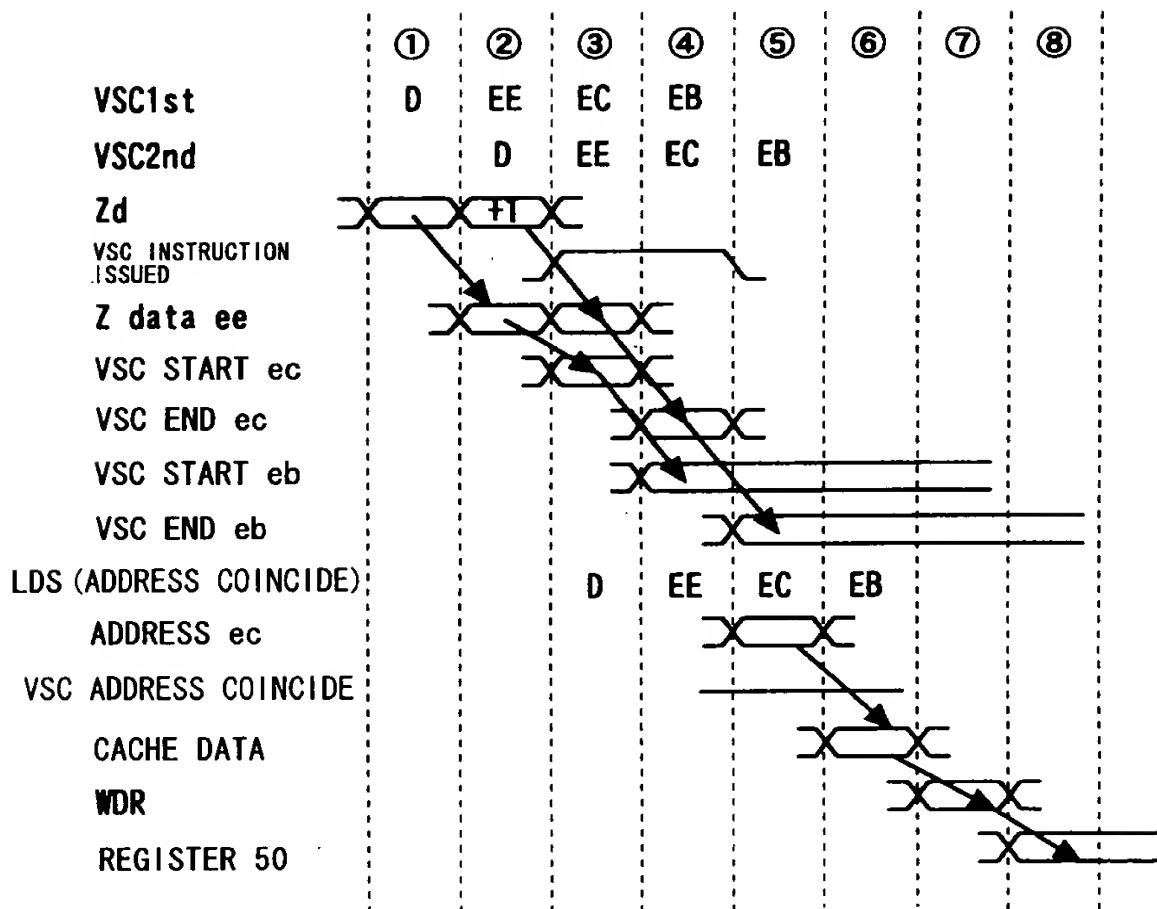


FIG . 5

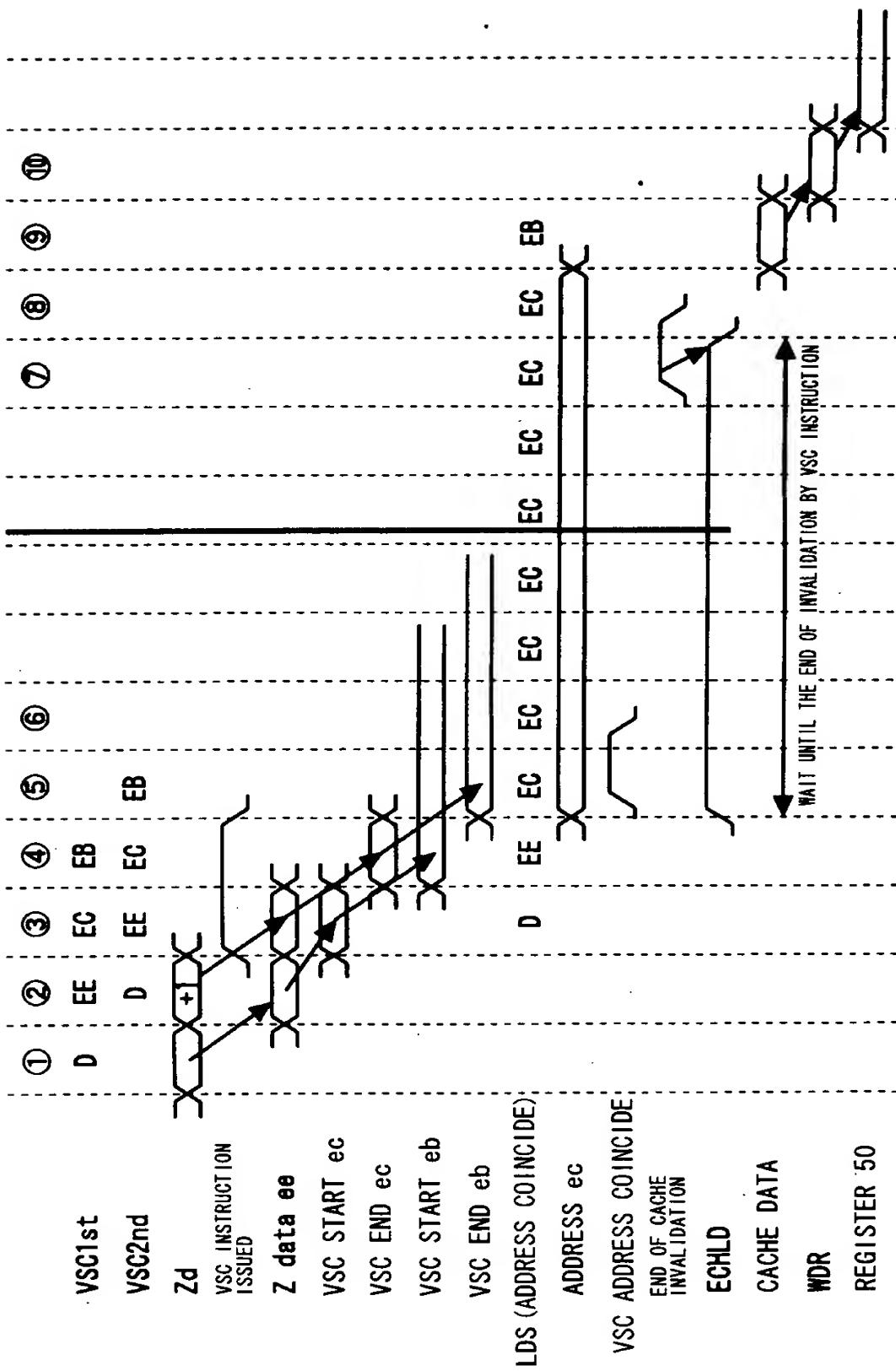


FIG . 6(a)

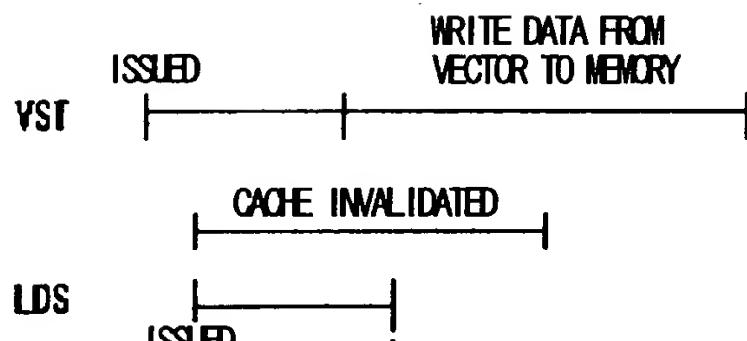


FIG . 6(b)

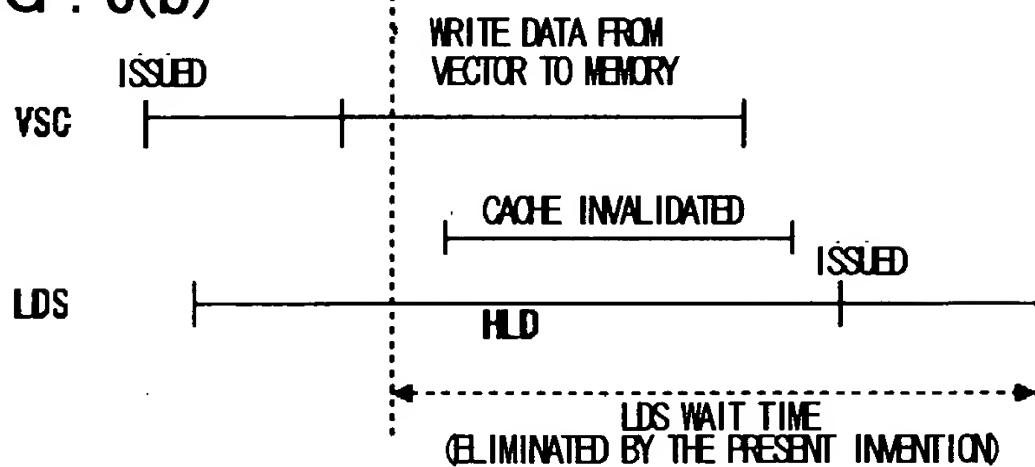


FIG . 7

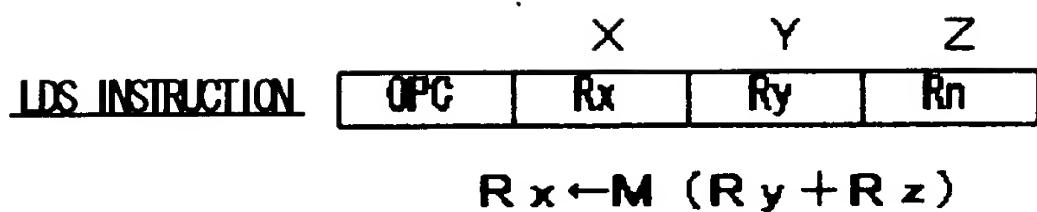
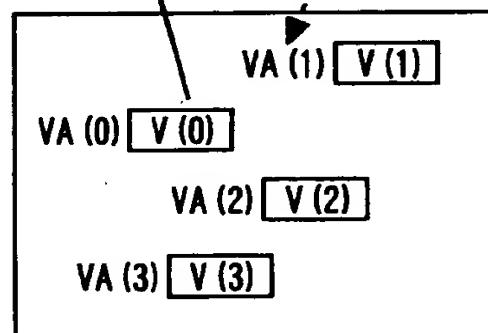
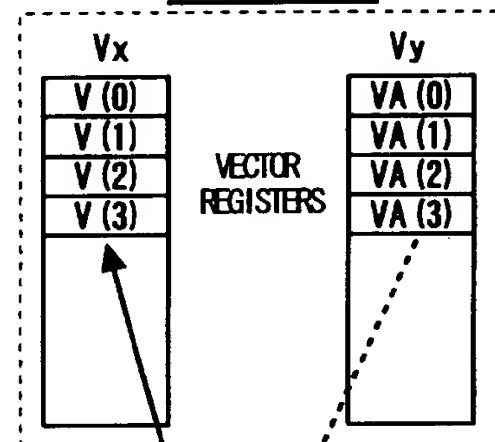


FIG . 8(a) PRIOR ART

VGT INSTRUCTION X Y Z
 OPC Vx Vy 

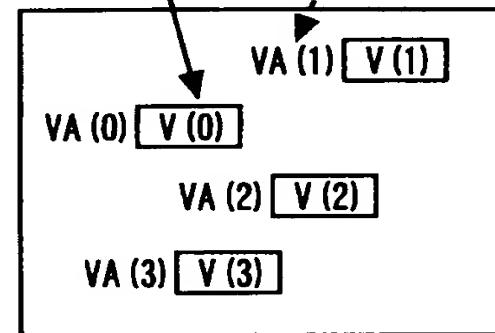
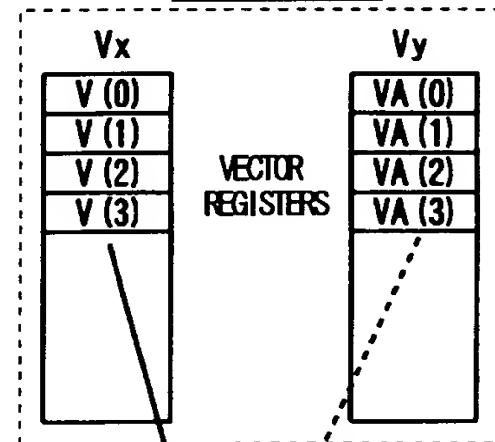
VECTOR UNIT



MEMORY

VSC INSTRUCTION X Y Z
 OPC Vx Vy 

VECTOR UNIT



MEMORY